Compensation for Operational Amplifier Circuits

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Inventor: Jeffrey S. Lehto

Docket No.: ELAN-01098US1

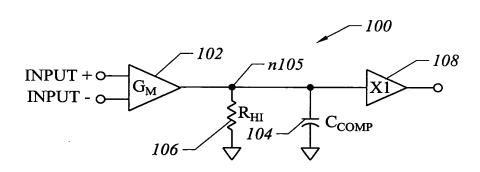


FIG. 1

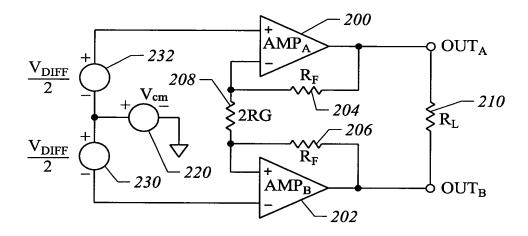
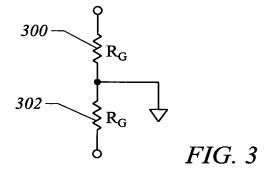


FIG. 2



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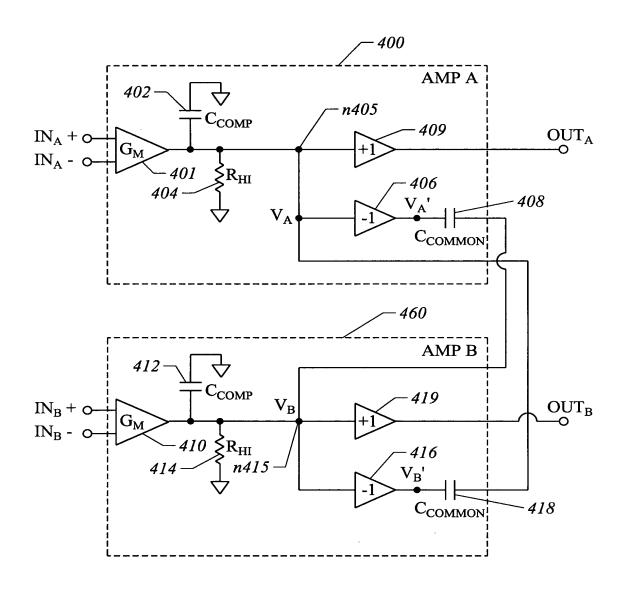


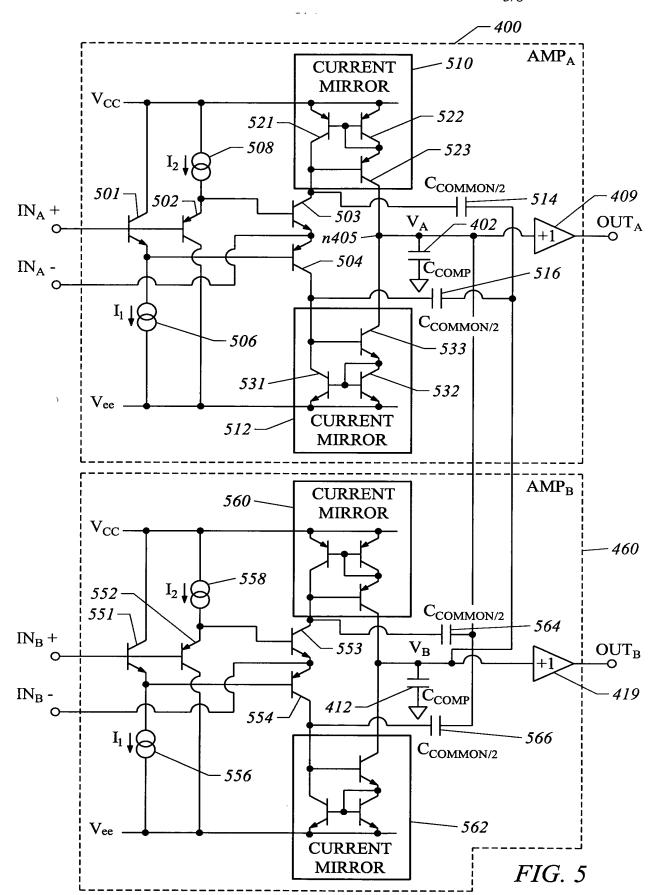
FIG. 4

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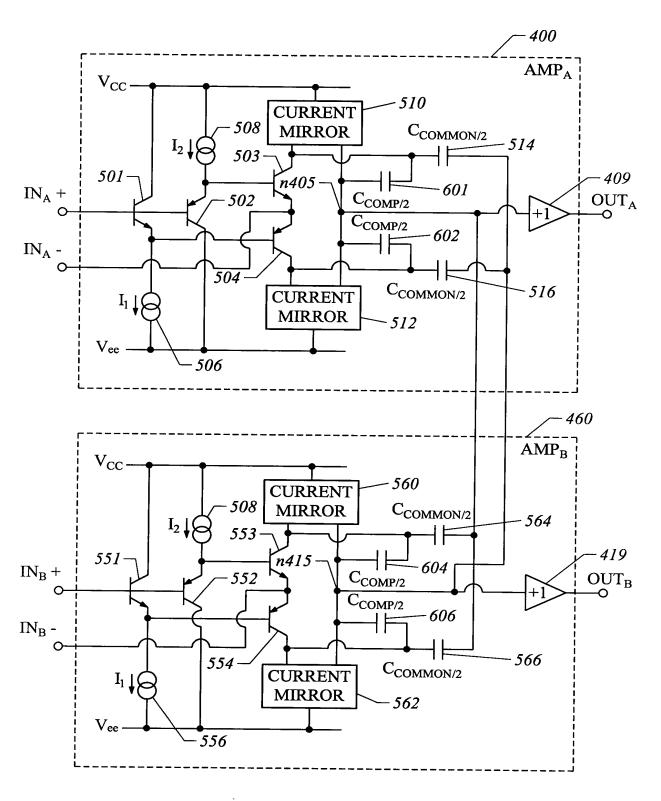


FIG. 6

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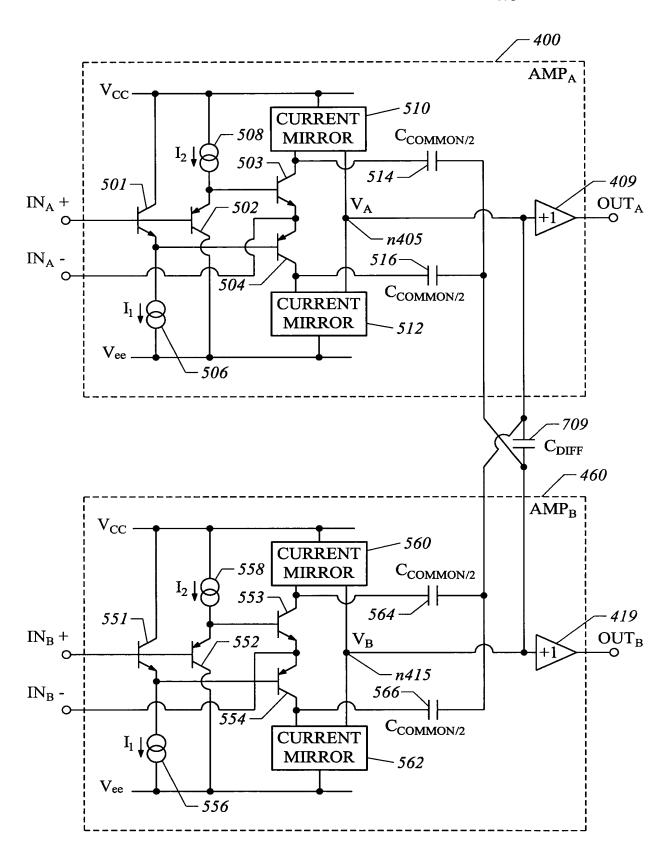
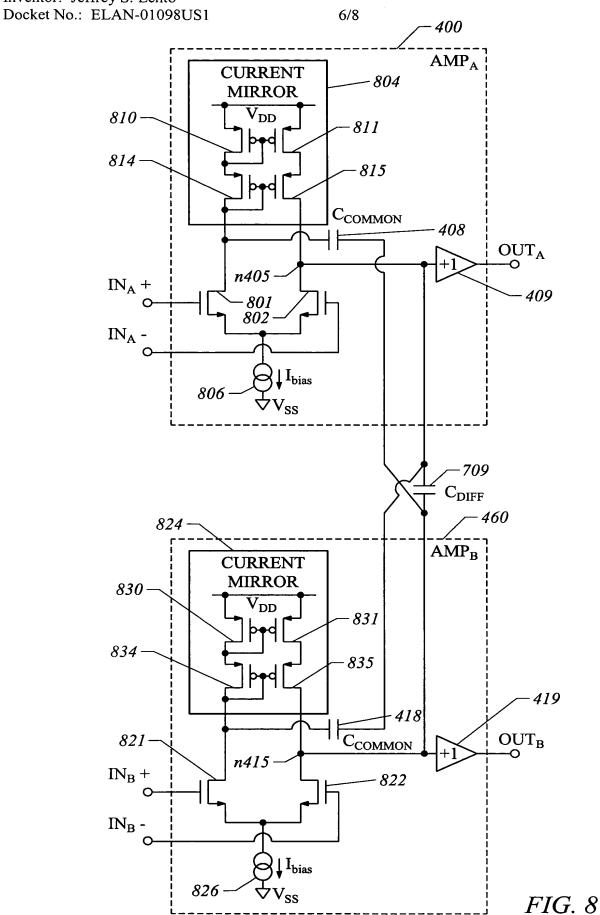


FIG. 7

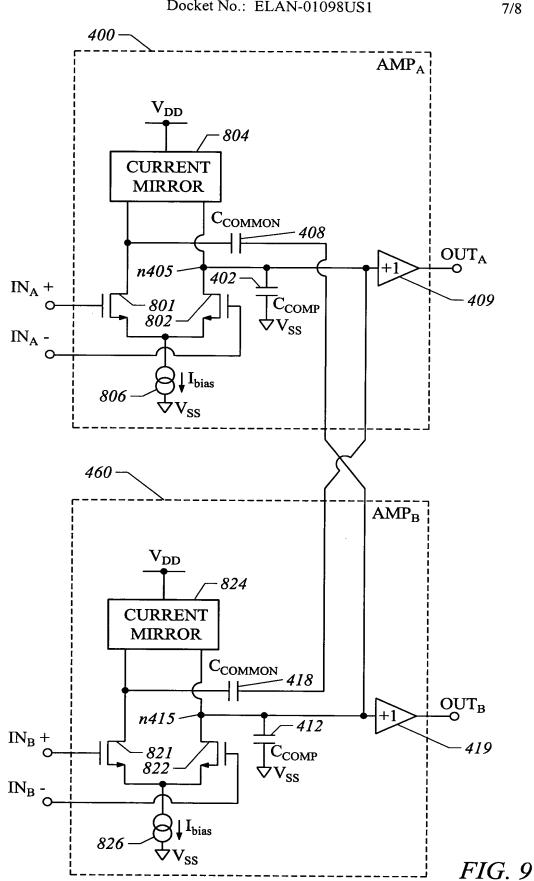
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8/8 400- $AMP_A$  $V_{DD}$ - 804 **CURRENT MIRROR** C<sub>COMMON</sub> 408  $OUT_A$ +1 $C_{COMP}$ 409 1002 n405 IN<sub>A</sub> + 801 802 IN<sub>A</sub> - $\downarrow I_{bias}$ 806  $\dot{\nabla} {
m V}_{
m SS}$ 460- $AMP_B$  $V_{DD}$ 824 **CURRENT MIRROR**  $C_{COMMON}$ 418  $OUT_B$  $C_{COMP}$ 1004 419 n415 IN<sub>B</sub> + 821 822 IN<sub>B</sub> - $\int I_{bias}$ 826 FIG. 10